

ABSTRACT OF THE DISCLOSURE

Disclosed is a semiconductor device comprising a bit line extending in a first direction, a plurality of transistors electrically connected to the bit  
5 line, a plurality of first electrodes arranged in the first direction and electrically connected to the transistors, a dielectric film covering upper and side surfaces of the first electrodes, and a second electrode covering the dielectric film, wherein a width  
10 of the first electrode is smaller than a distance between adjacent first electrodes and smaller than the minimum value of design rule of the semiconductor device.